

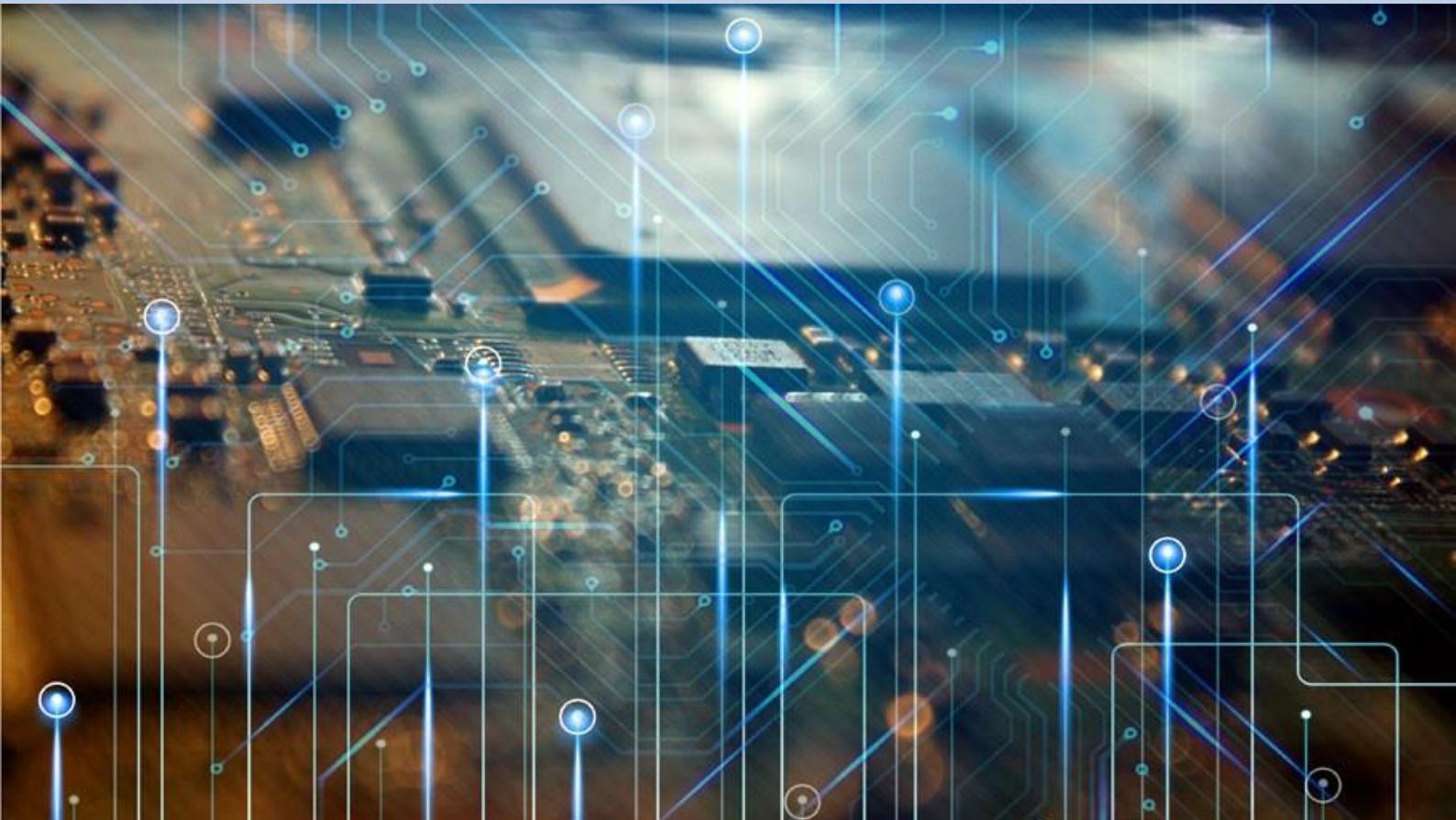
60V N-Channel Power MOSFET

MOSFET

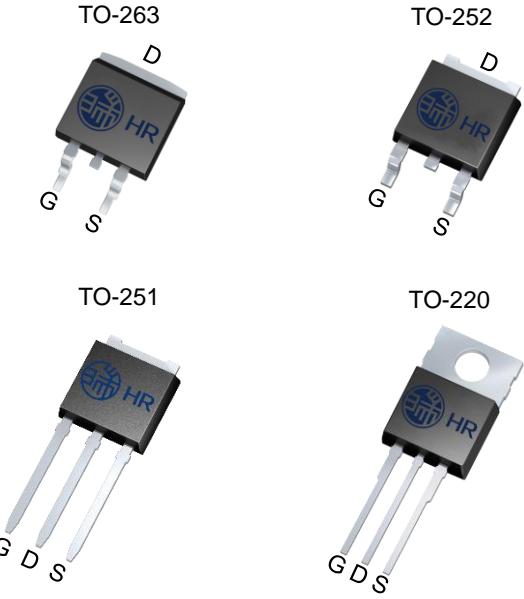
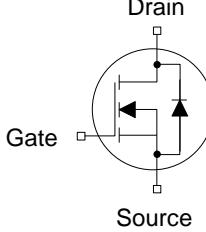
Metal Oxide Semiconductor Field Effect Transistor

HRT60N10x Data Sheet

Rev. 2020 V1.0



60V N-Channel Power MOSFET

<p>Description</p> <p>N-Channel Power MOSFET designed by HR-Micro Semiconductor Company, according to the advanced Trench Technology. This devices provide an excellent Gate charge and $R_{DS(on)}$, which leads to extremely low communication and conduction losses. So it is very suitable for AC/DC power conversion, load switch and industrial power applications.</p>	 <p>TO-263 TO-252 D G S D G S D G S D G S</p>	
<p>Features</p> <ul style="list-style-type: none"> Low FOM $R_{DS(on)} \times Q_{gd}$ 100% avalanche tested Easy to use/drive RoHS compliant 		
<p>Applications</p> <ul style="list-style-type: none"> DC-DC Converter Battery Protection Charge/Discharge Load Switch Synchronous Rectification 	 	
<p>Key Performance Parameters</p>		
Parameter	Value	Unit
$V_{DS@T_c=25^\circ C}$	60	V
$R_{DS(on),max@10V}$	10	$\text{m}\Omega$
$Q_{g,typ}$	76	nC
$I_D@T_c=25^\circ C$	75	A
$I_{D,pulse}$	300	A
$E_{AS}^1)$	258	mJ
<p>Device Marking and Package Information</p>		
Device	Package	Marking
HRT60N10B	TO263	60N10B
HRT60N10D	TO252	60N10D
HRT60N10U	TO251	60N10U
HRT60N10P	TO220	60N10P

Absolute Maximum Ratings $T_A = 25^\circ\text{C}$, unless otherwise noted			
Parameter	Symbol	Values	Unit
Drain-Source voltage($V_{GS}=0\text{V}$)	V_{DS}	60	V
Continuous Drain Current ²⁾	I_D	75	A
$T_C = 100^\circ\text{C}$		48	
Pulsed Drain Current ³⁾	$I_{D,pulse}$	300	A
Gate-Source Voltage	V_{GSS}	$\pm 20\text{V}$	V
Single Pulse Avalanche Energy ¹⁾	E_{AS}	258	mJ
Power Dissipation	P_D	105	W
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55~+150	°C

Thermal Resistance			
Parameter	Symbol	Max.	Unit
Thermal Resistance, Junction-to-Case	R_{thJC}	1.5	°C/W
Thermal Resistance, Junction-to-Ambient	R_{thJA}	50	°C/W

Notes

- 1) $L=0.5\text{mH}, V_{DD}=30\text{V}$, Start $T_J=25^\circ\text{C}$
- 2) Limited by maximum junction temperature.
- 3) Repetitive Rating: Pulse width limited by maximum junction temperature.

Electrical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted						
Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
Static Characteristics						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	60	--	--	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}} = 60\text{V}$ $V_{\text{GS}} = 0\text{V}, T_J = 25^\circ\text{C}$	--	--	1	μA
		$V_{\text{DS}} = 48\text{V}$, $V_{\text{GS}} = 0\text{V}, T_J = 125^\circ\text{C}$	--	--	100	
Gate-Source Leakage Current	I_{GSS}	$V_{\text{GS}} = \pm 20\text{V}$	--	--	± 100	nA
Gate-Source Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	2	3	4	V
Drain-Source On-State-Resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = 10\text{V}, I_D = 20\text{A}$	--	7.5	10	$\text{m}\Omega$
Gate Resistance	R_G	$f = 1.0\text{MHz}$ open drain	--	2.2	--	Ω
Dynamic Characteristics						
Input Capacitance	C_{iss}	$V_{\text{GS}} = 0\text{V},$ $V_{\text{DS}} = 30\text{V}$ $f = 1.0\text{MHz}$	--	4938	--	pF
Output Capacitance	C_{oss}		--	234	--	
Reverse Transfer Capacitance	C_{rss}		--	167	--	
Total Gate Charge	Q_g	$V_{\text{DS}} = 30\text{V}, I_D = 20\text{A}$ $V_{\text{GS}} = 10\text{V}$	--	76	--	nC
Gate-Source Charge	Q_{gs}		--	22	--	
Gate-Drain Charge	Q_{gd}		--	14	--	
Gate Plateau Voltage	V_{Plateau}		--	4.8	--	V
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DS}} = 30\text{V}, V_{\text{GS}} = 10\text{V}$ $R_G = 3\Omega, I_D = 20\text{A}$	--	18	--	ns
Turn-on Rise Time	t_r		--	39	--	
Turn-off Delay Time	$t_{\text{d}(\text{off})}$		--	45	--	
Turn-off Fall Time	t_f		--	28	--	
Drain-Source Body Diode Characteristics						
Body Diode Forward Voltage	V_{SD}	$T_J = 25^\circ\text{C}, I_{\text{SD}} = 20\text{A},$ $V_{\text{GS}} = 0\text{V}$	--	--	1.2	V
Continuous Diode Forward Current	I_S		--	--	75	A
Reverse Recovery Time	t_{rr}	$I_F = 20\text{A}, \frac{di_F}{dt} = 100\text{A}/\mu\text{s}$	--	27	--	ns
Reverse Recovery Charge	Q_{rr}		--	49	--	nC

Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

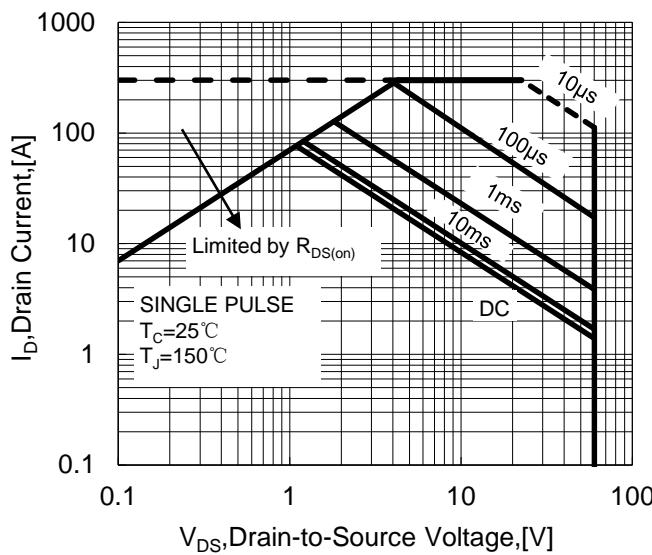


Figure 1. Maximum Safe Operating Area

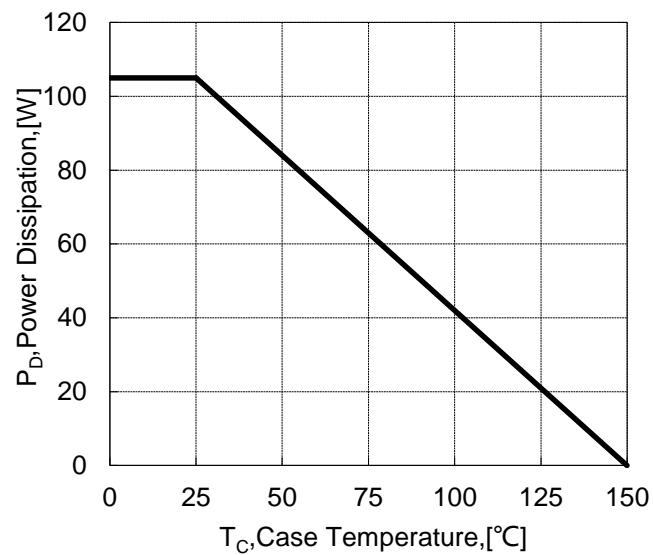


Figure 2. Maximum Power Dissipation vs Case Temperature

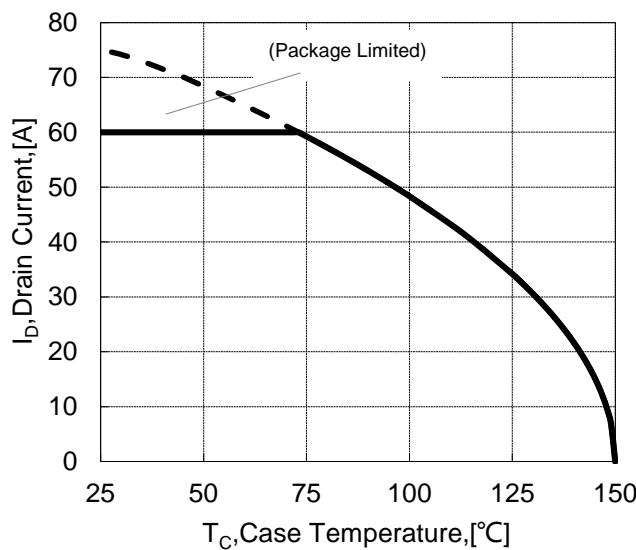


Figure 3. Maximum Continuous Drain Current vs Case Temperature

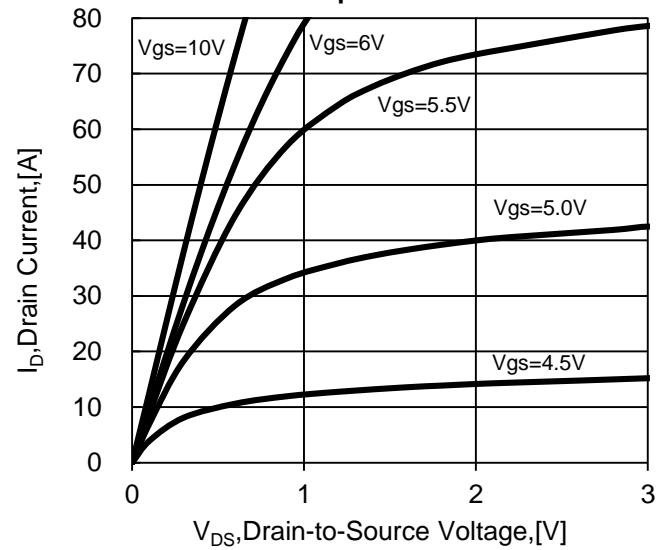


Figure 4. Typical output Characteristics

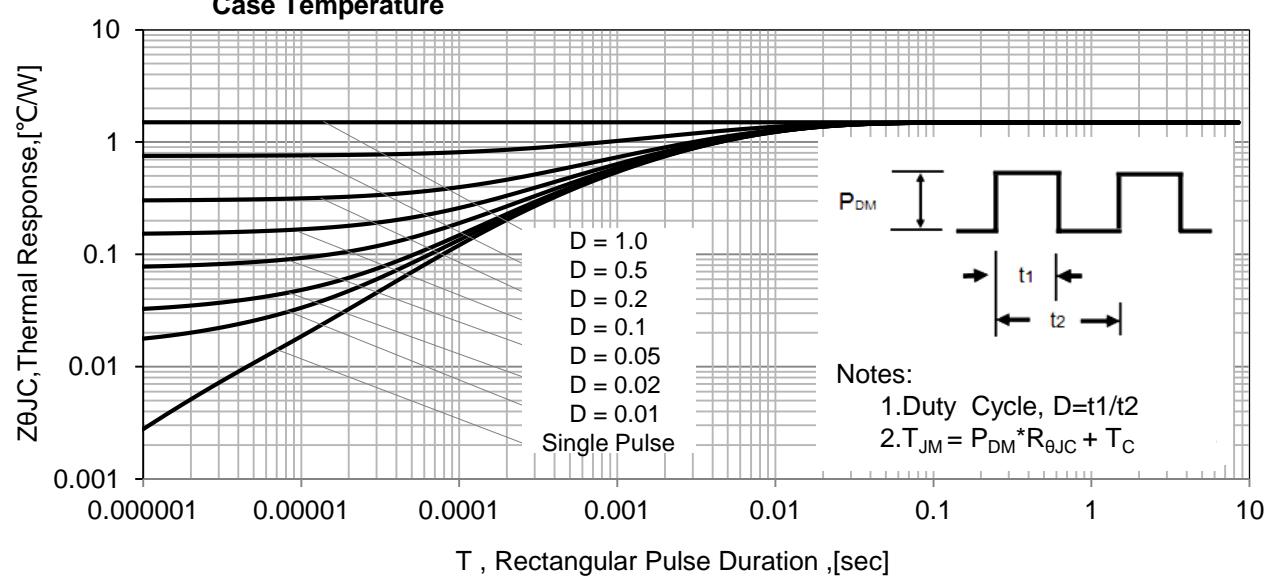


Figure 5 Maximum Effective Thermal Impedance , Junction to Case

Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

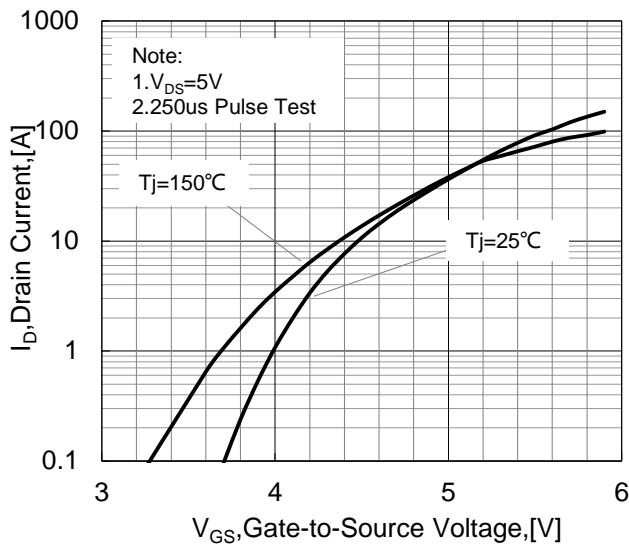


Figure 6 Typical Transfer Characteristics

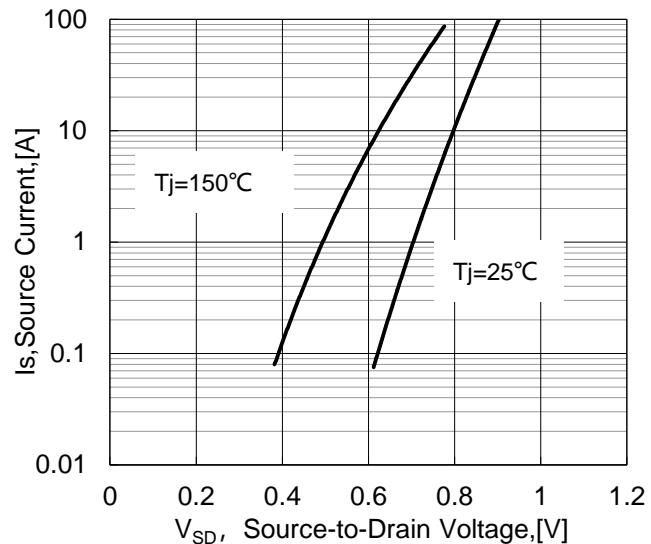


Figure 7 Typical Body Diode Transfer Characteristics

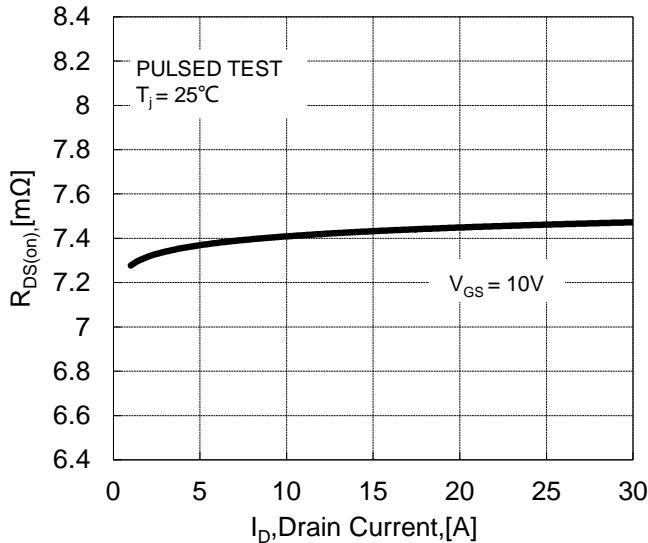


Figure 8. Drain-to-Source On Resistance vs Drain Current

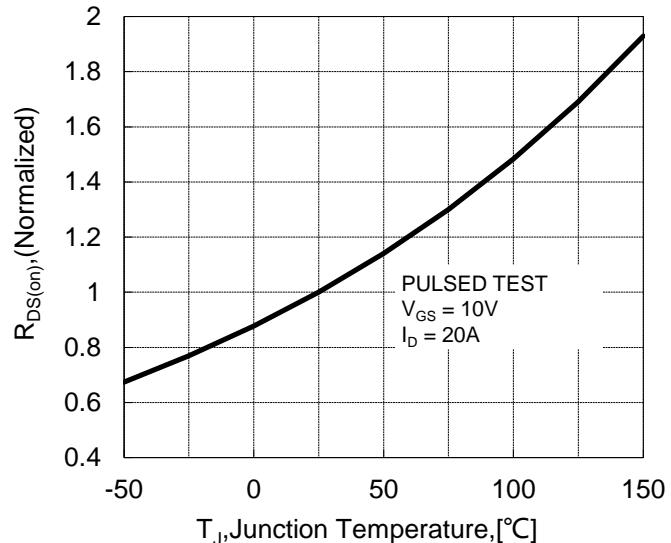


Figure 9. Normalized On Resistance vs Junction Temperature

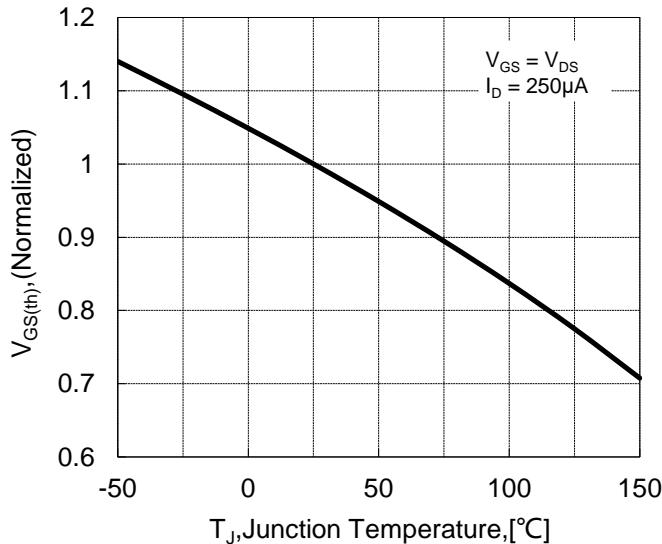


Figure10. Normalized Threshold Voltage vs Junction Temperature

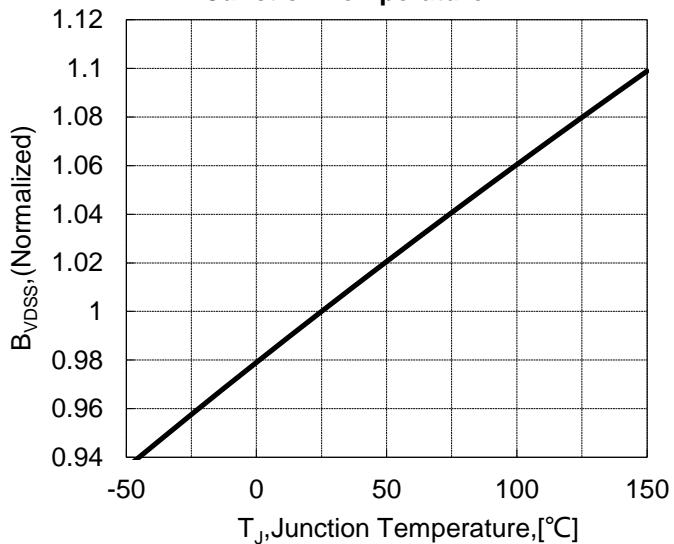


Figure 11. Normalized Breakdown Voltage vs Junction Temperature

Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

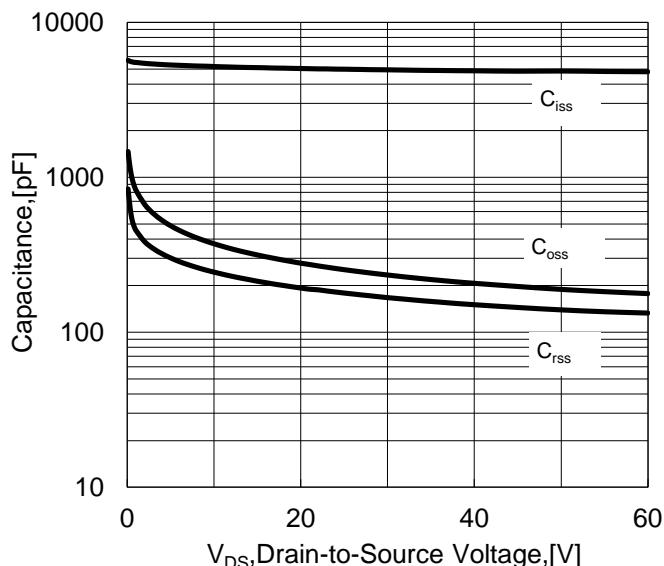


Figure 12. Capacitance Characteristics

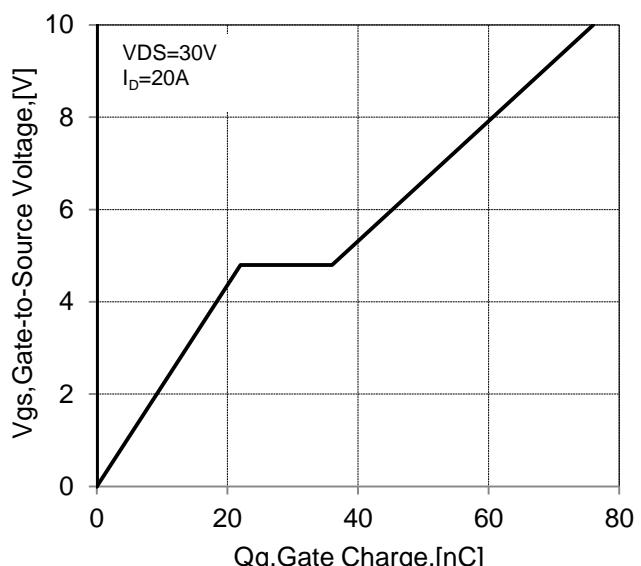


Figure 13 Typical Gate Charge vs Gate to Source Voltage

Figure A: Gate Charge Test Circuit and Waveform

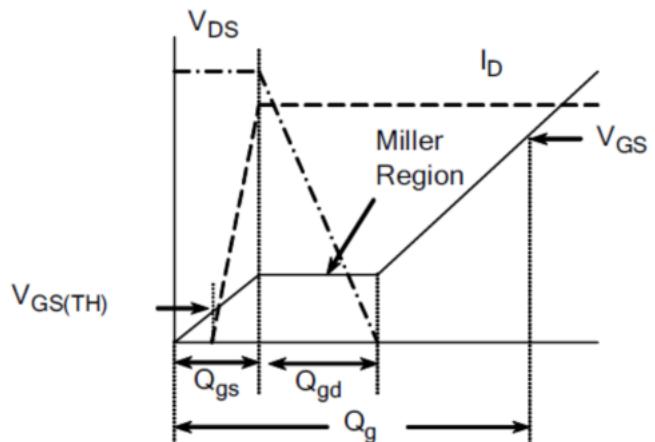
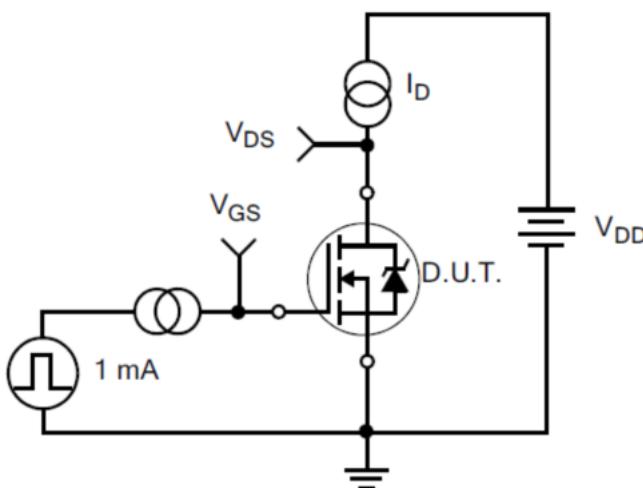


Figure B: Resistive Switching Test Circuit and Waveform

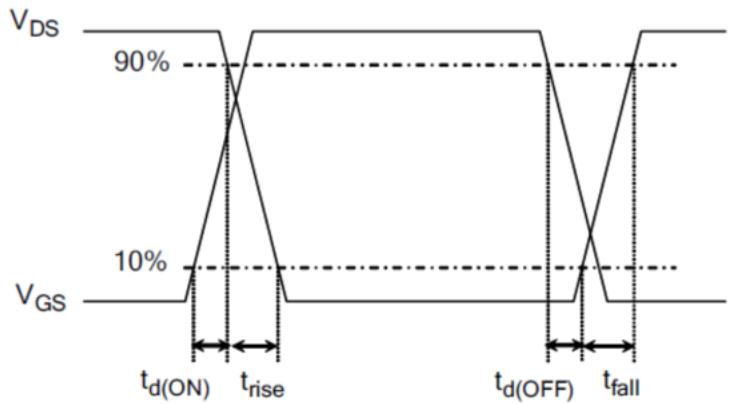
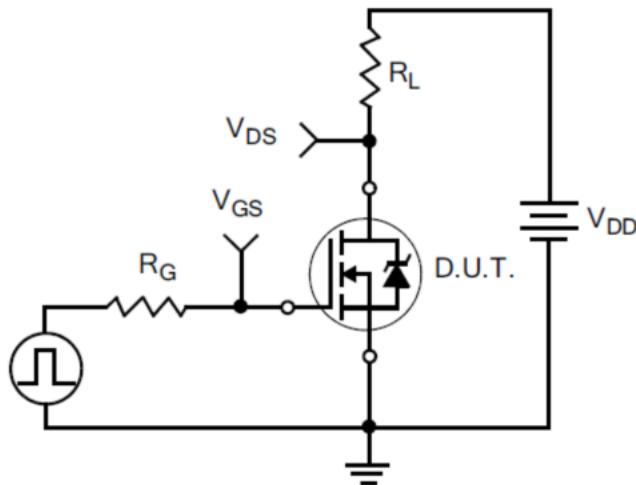
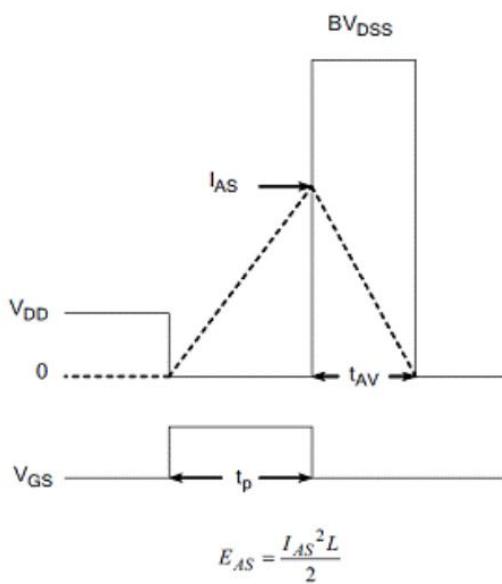
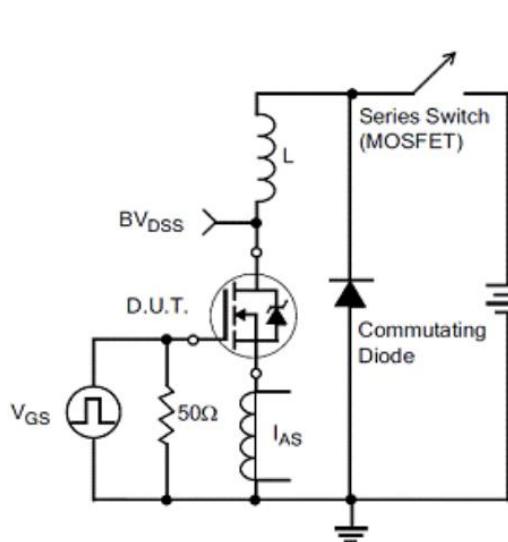
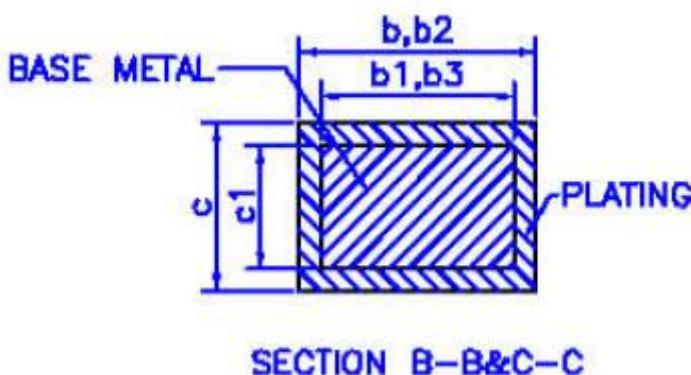
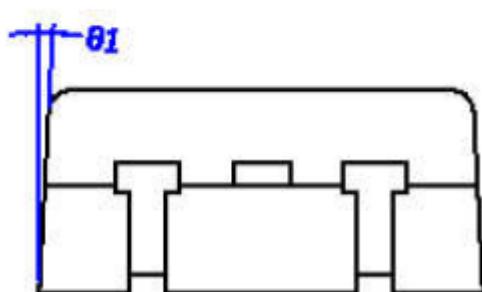
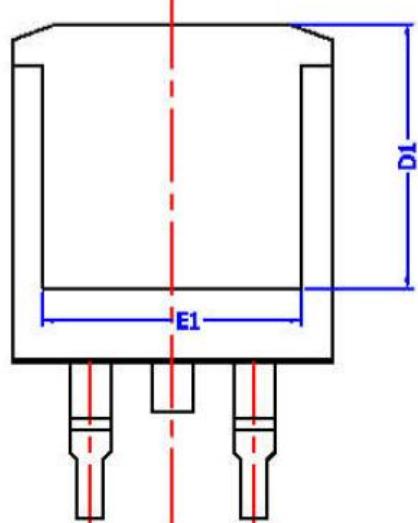
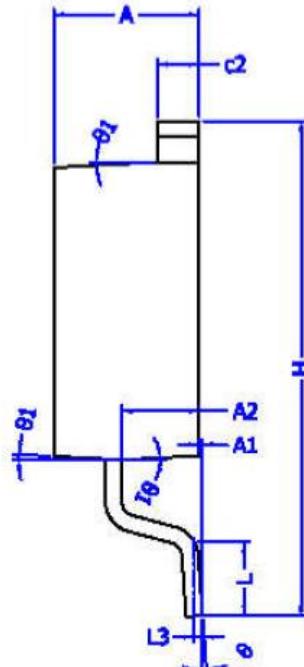
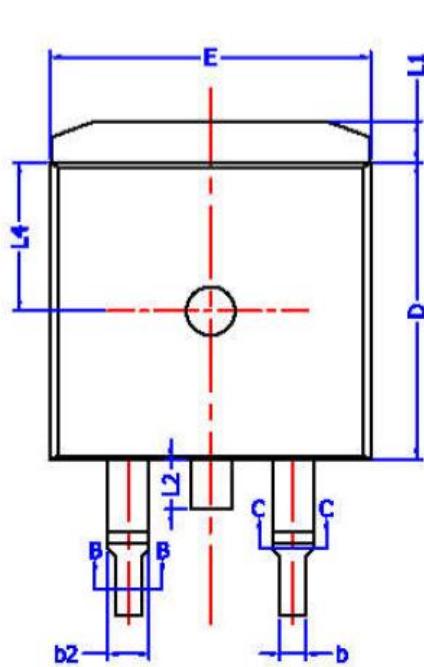


Figure C: Unclamped Inductive Switching Test Circuit and Waveform

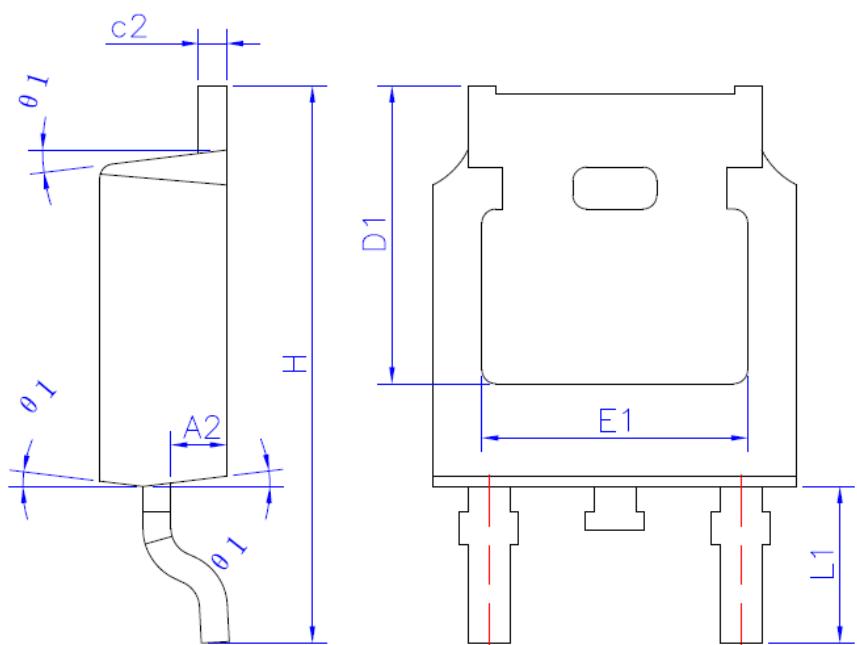
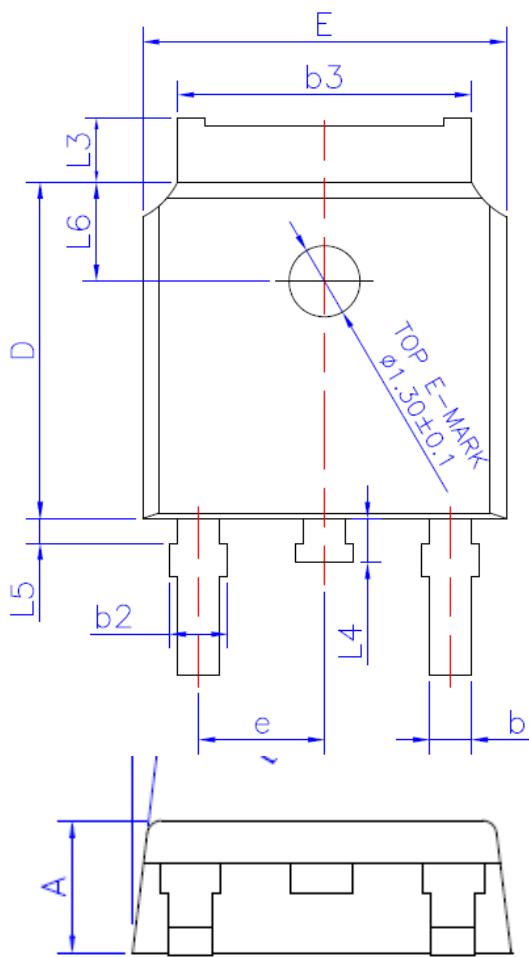


TO263 Package

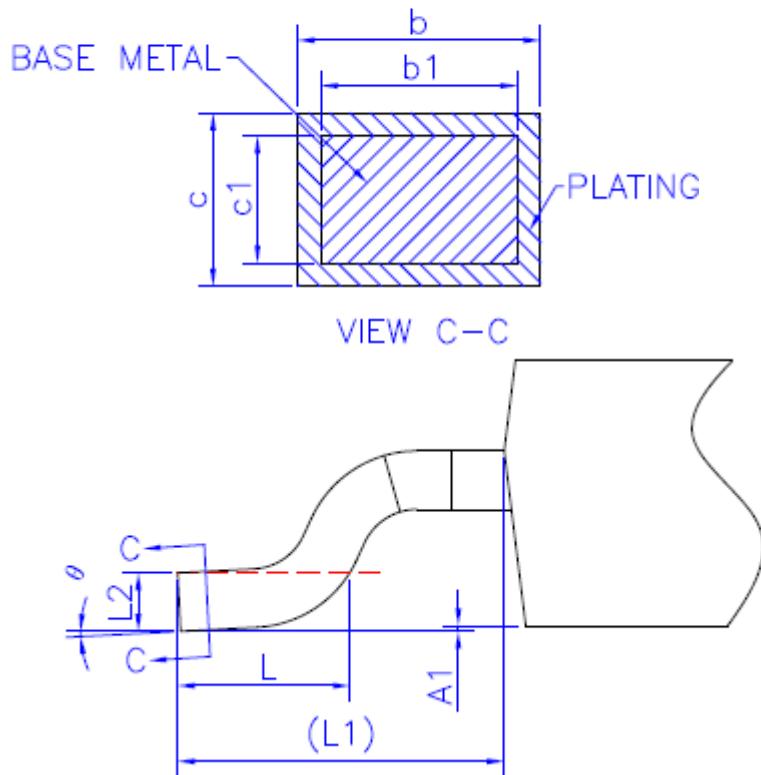


SYMBOL	MIN	NOM	MAX
A	4.40	4.50	4.60
A1	0	0.10	0.25
A2	2.20	2.40	2.60
b	0.76	--	0.89
B1	0.75	0.80	0.85
B2	1.23	--	1.37
b3	1.22	1.27	1.32
c	0.47	--	0.60
c1	0.46	0.51	0.56
c2	1.25	1.30	1.35
D	9.10	9.20	9.30
D1	8.00	--	--
E	9.80	9.90	10.00
E1	7.80	--	--
e	2.54 BSC		
H	14.90	15.30	15.70
L	2.00	2.30	2.60
L1	1.17	1.27	1.40
L2	--	--	1.75
L3	0.25 BSC		
L4	4.60 REF		
θ	0°	--	8°
θ_1	1°	3°	5°

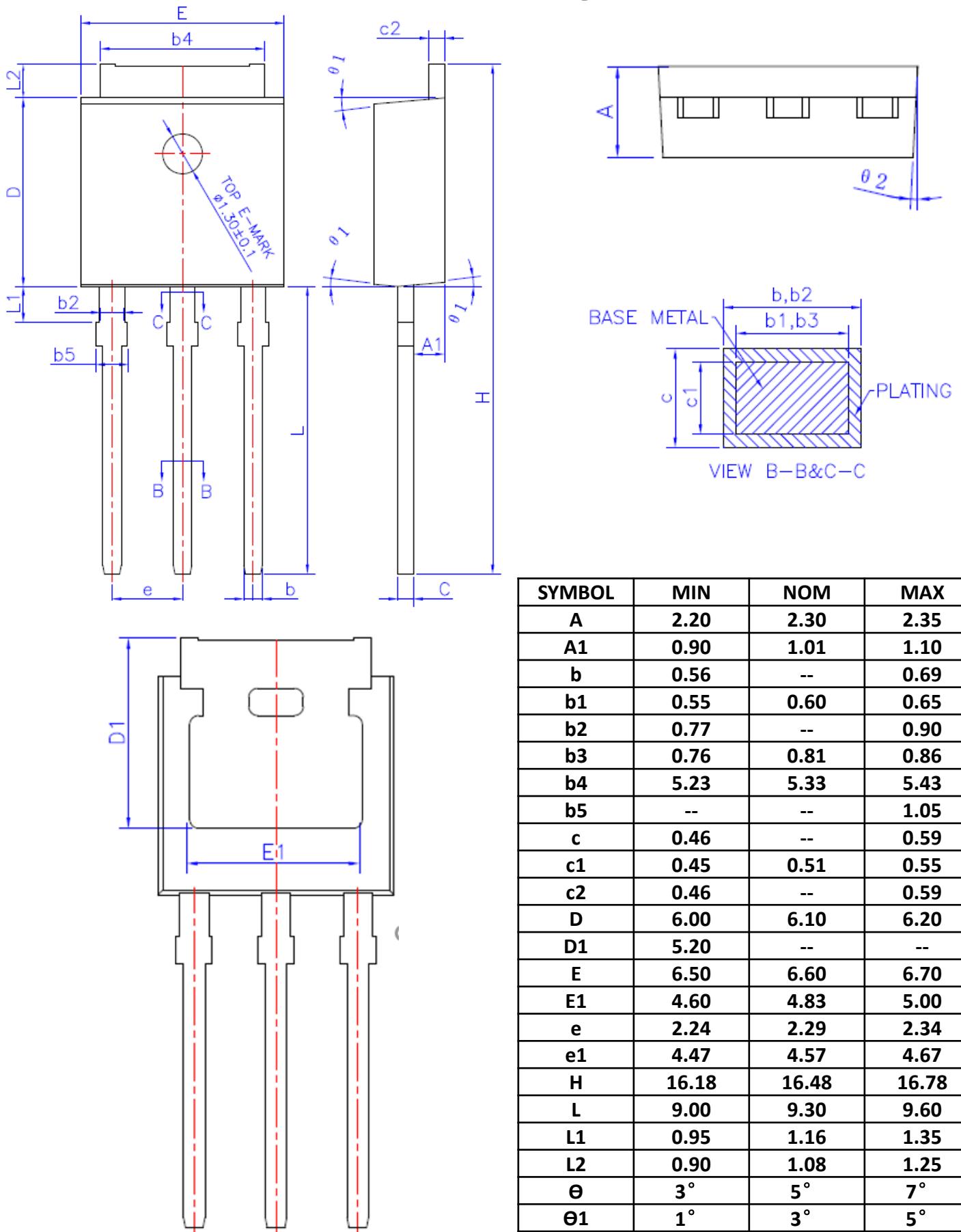
TO252 Package



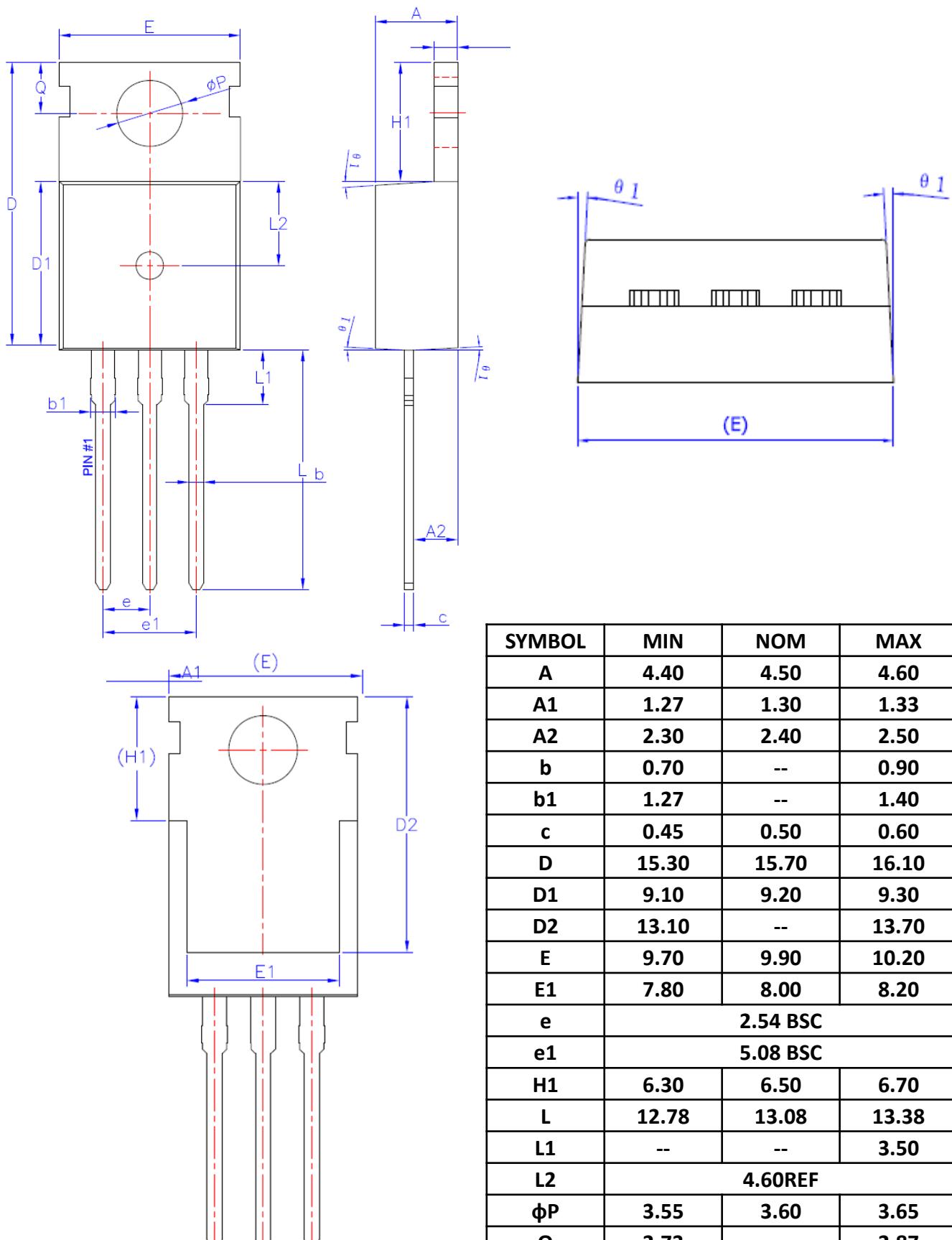
SYMBOL	MIN	NOM	MAX
A	2.20	2.30	2.38
A1	0	--	0.10
A2	0.90	1.01	1.10
b	0.72	--	0.85
b1	0.71	0.76	0.81
b2	0.72	--	0.90
b3	5.13	5.33	5.46
c	0.47	--	0.60
c1	0.46	0.51	0.56
c2	0.47	--	0.60
D	6.00	6.10	6.20
D1	5.25	--	--
E	6.50	6.60	6.70
E1	4.70	--	--
e	2.186	2.286	2.386
H	9.80	10.10	10.40
L	1.40	1.50	1.70
L1	2.90 REF		
L2	0.508 BSC		
L3	0.90	--	1.25
L4	0.60	0.80	1.00
L5	0.15	--	0.75
L6	1.80 REF		
theta	0°	--	8°
theta1	5°	7°	9°
theta2	5°	7°	9°



TO251 Package



TO220 Package



Disclaimer

HRM has made reasonable commercial efforts to ensure that the information given in this datasheet is correct. However, it must clearly be understood that such information is for guidance only and does not constitute any representation or form part of any offer or contract.

For documents and material available from this datasheet, HRM does not warrant or assume any legal liability or responsibility for the accuracy, completeness, or usefulness of any information, product, technology or process disclosed hereunder.

HRM reserves the rights to at its own discretion to make any changes or improvements to this datasheet. Unless said datasheet is incorporated into the formal contract, any customer should not rely on the information as any specification or product parameters duly committed by HRM. Customers are hereby advised to verify that the information contained herein is current and complete before the entering of any contract or acknowledgement of any purchase order. Accordingly, all products specified hereunder shall be sold subject to HRM's terms and conditions supplied at the time of order acknowledgement. Except where agreed upon by contractual agreement, testing of all parameters of each product is not necessarily performed.

HRM does not warrant or convey any license either expressed or implied under its patent rights, nor the rights of others. Reproduction of information contained herein shall be only permissible if such reproduction is without any modification or alteration. Reproduction of this information with any alteration is an unfair and deceptive business practice. HRM is not responsible or liable for such altered documentation.

Resale of HRM's products with statements different from or beyond the parameters stated by HRM for that product or service voids all express or implied warranties for the associated HRM's product or service and is unfair and deceptive business practice. HRM is not responsible or liable for any such statements.

HRM's products are not authorized for use as critical components in life support devices or systems without the express written approval of HRM. As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and whose failure to perform, when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.